

# Abstracts

## RF mixers using standard digital CMOS 0.35 /spl mu/m process

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V. Geffroy, G. De Astis and E. Bergeault. "RF mixers using standard digital CMOS 0.35 /spl mu/m process." 2001 MTT-S International Microwave Symposium Digest 01.1 (2001 Vol. I [MWSYM]): 83-86 vol.1.

The performance of doubly-balanced active and resistive RF CMOS mixers is presented following measurements at 1.8-GHz RF frequency. The active mixer has demonstrated 10.4-dB gain, 7-dB SSB NF, -6 dBm IIP3 and consumes 9.7 mA under 3 V. The passive mixer has demonstrated 7.5-dB loss, 10-dB SSB NF and +16-dBm of input IP3.

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